		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/017,793	WU ET AL.
		Examiner	Art Unit	
		William M. Treat	2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rotenberg et al., Trace Processors, Thirtieth Annual IEEE/ACM International Symposium on Microarchitecture, Dec. 1-3, 1997, pp. 138-148.
	V	Sundarmoorthy et al., Multipath Execution on Chip Multiprocessors Enabled by Redundant Threads, Technical Report CESR-TR-01-2, Oct. 23, 2001, pp. 1-19.
	W	Rotenberg, AR-SMT: A Microarchitectural Approach to Fault Tolerance in Microprocessors, 29th Annual International Symposium on Fault-Tolerant Computing, June 15-18, 1999, pp. 84-91.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.